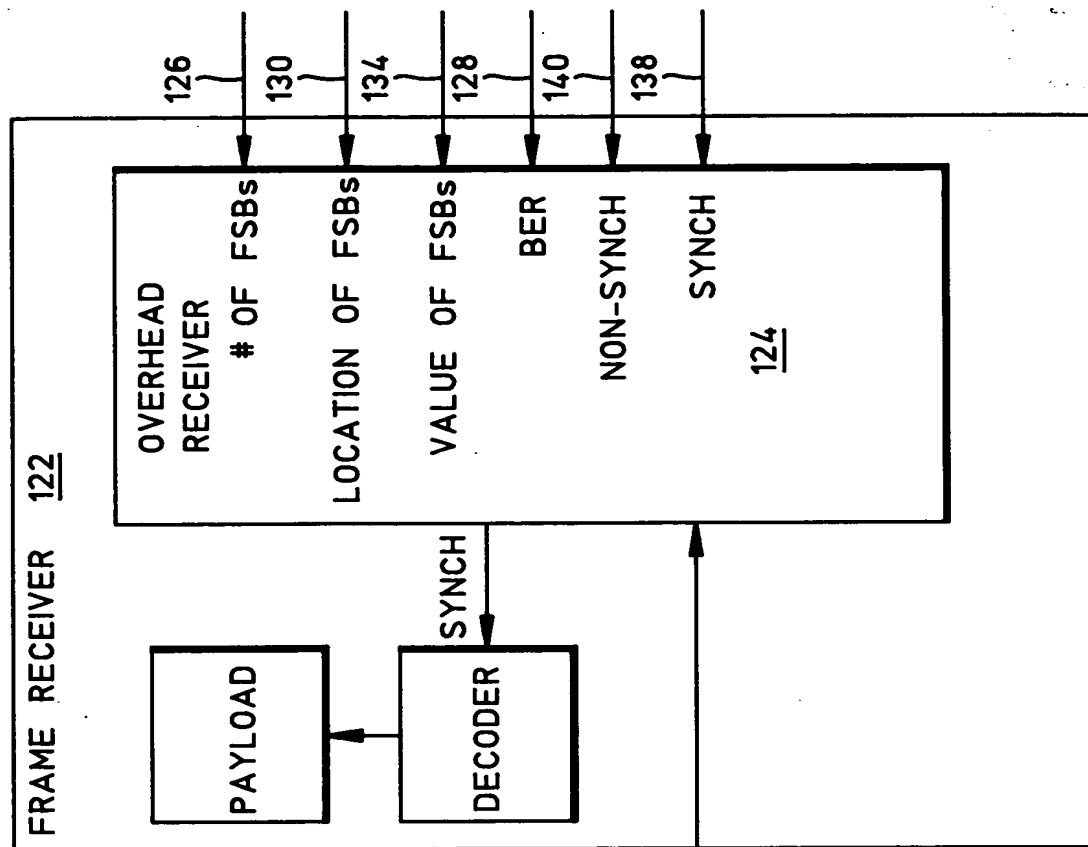
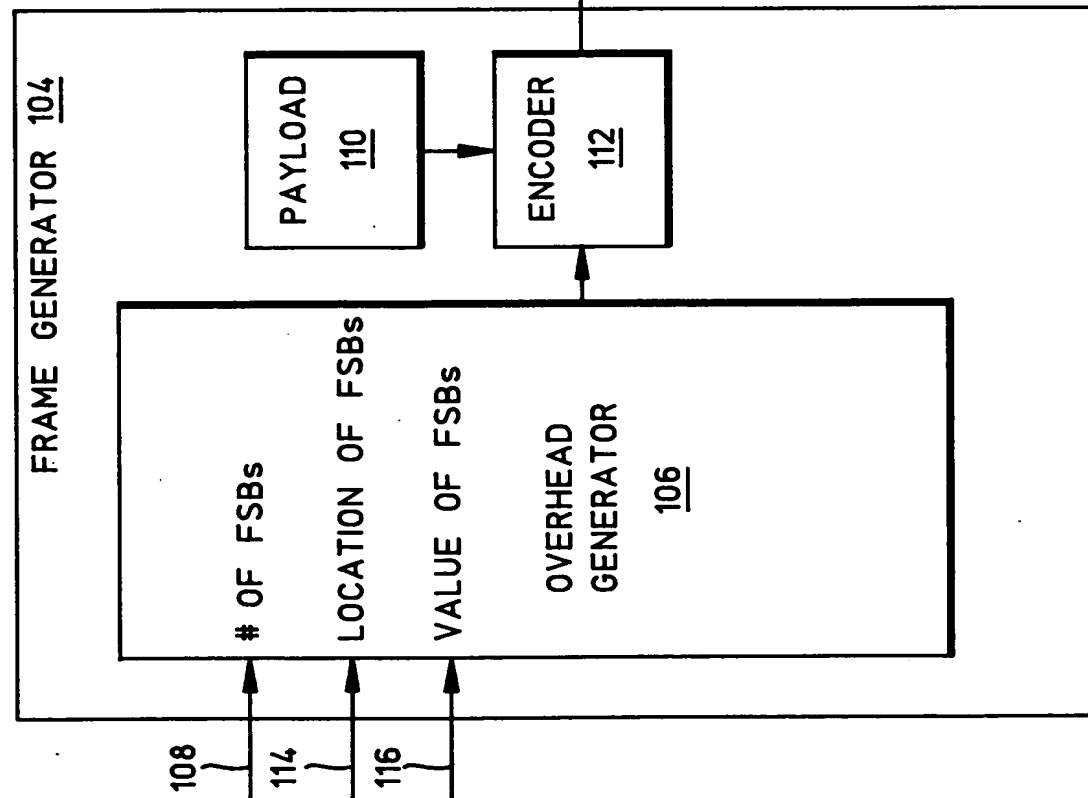


FIG. 1 is a block diagram of a system 100 for transmitting and receiving data frames.

100

TRANSMITTER 102

RECEIVER 120



134 → 116
132 → 114

FIG. 1

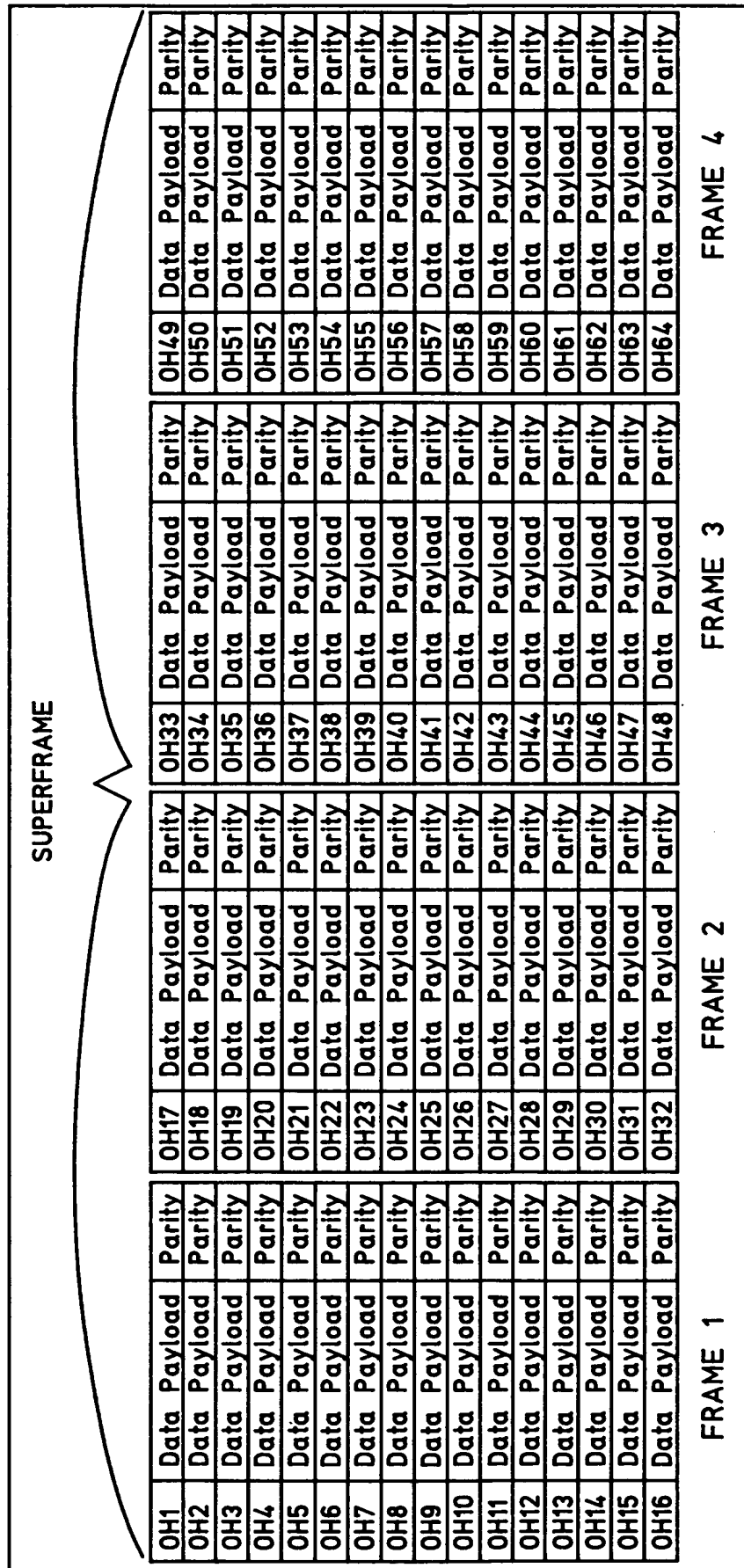


FIG. 2

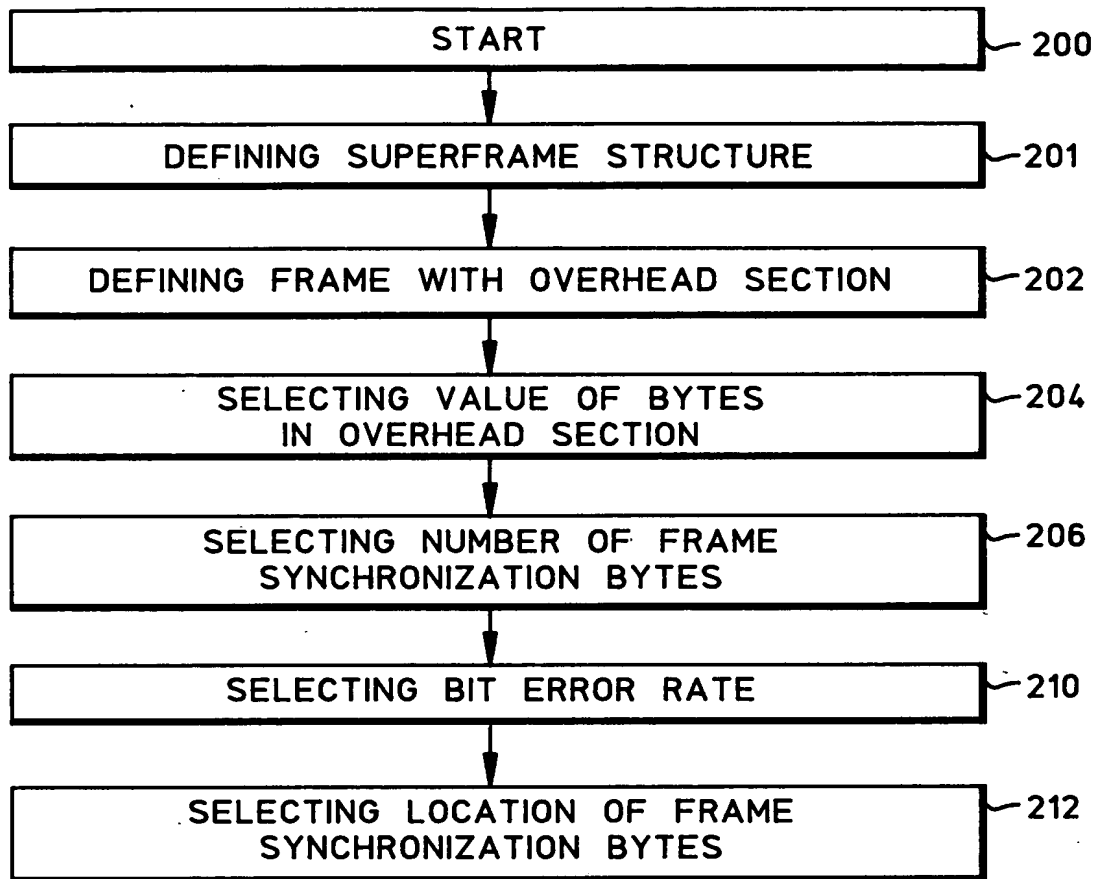


FIG. 3

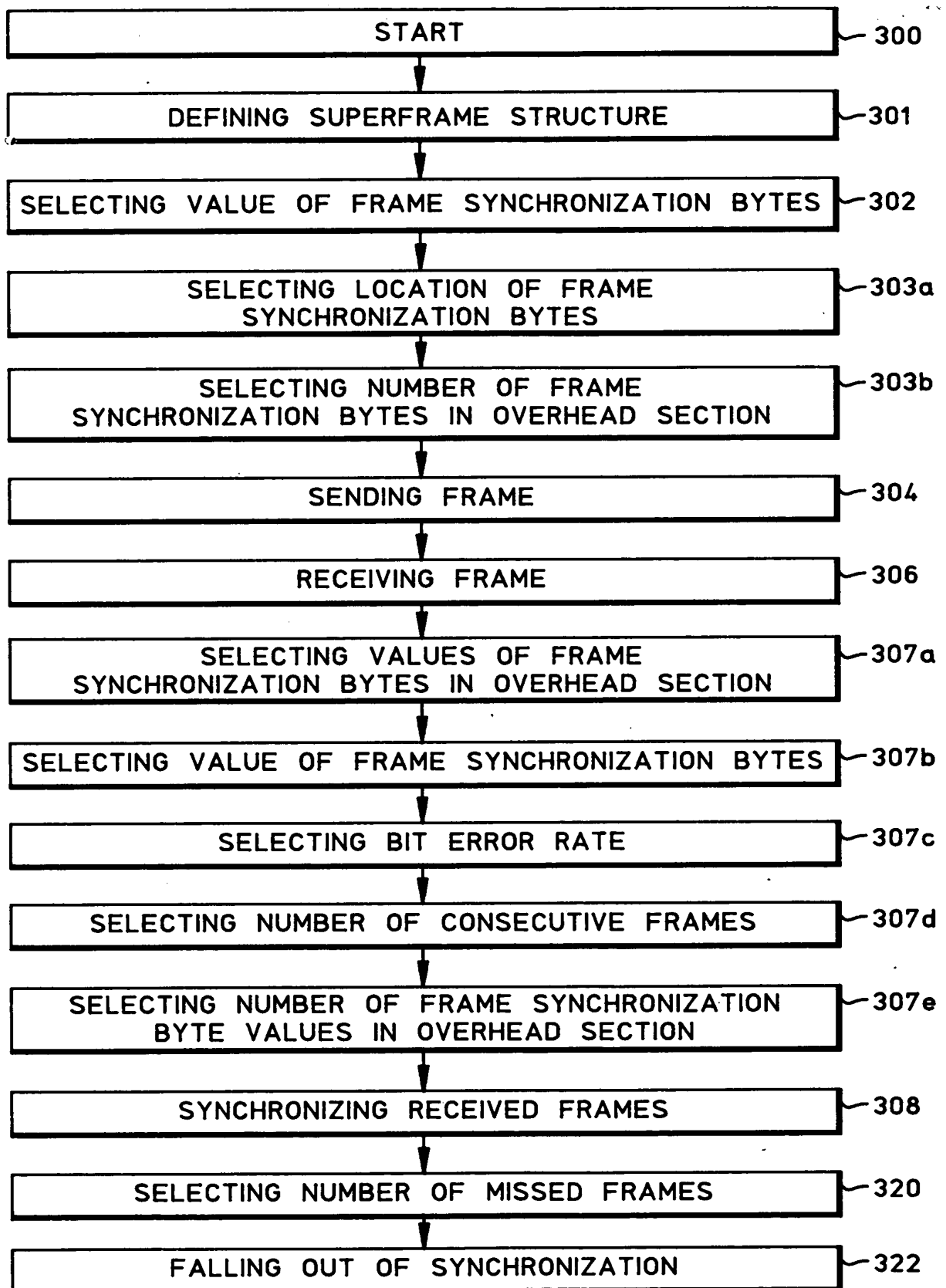


FIG. 4

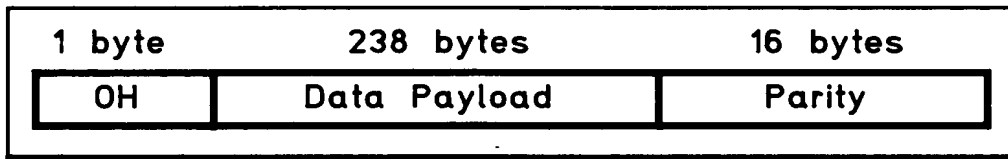


FIG. 5

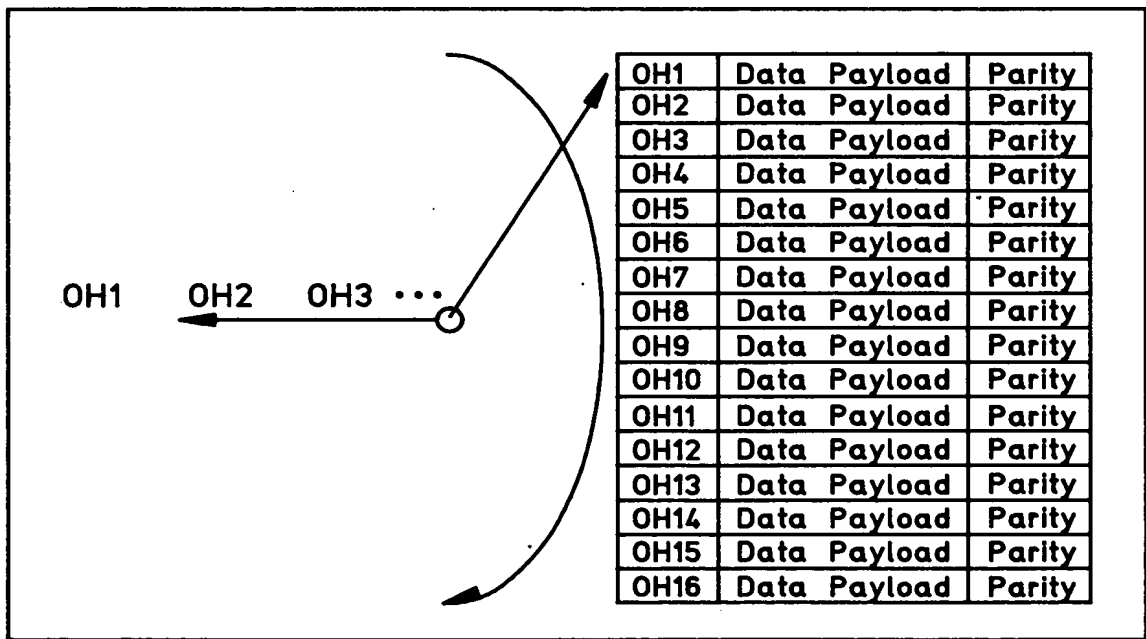


FIG. 6